

(19)



Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) Publication number:

**0 415 659 A2**

(12)

**EUROPEAN PATENT APPLICATION**

(21) Application number: 90309293.0

(51) Int. Cl.<sup>5</sup>: **H01L 21/48, H01L 23/498**

(22) Date of filing: 24.08.90

(30) Priority: 28.08.89 JP 218723/89

(43) Date of publication of application:  
06.03.91 Bulletin 91/10(84) Designated Contracting States:  
DE FR GB

(71) Applicant: **SUMITOMO METAL MINING  
COMPANY LIMITED**  
11-3, Shinbashi 5-chome Minato-ku  
Tokyo 105(JP)

(72) Inventor: **Chonan, Takeshi**  
3-9-1 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)  
Inventor: **Kudo, Yuko**  
2-1-8 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)  
Inventor: **Hirota, Yoshihiro**  
3-18-35 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)

(74) Representative: **Hartley, David et al**  
**Withers & Rogers 4 Dyer's Buildings Holborn**  
- London, EC1N 2JT(GB)

(54) **Process for making a two-layer film carrier.**

(57) A two-layer film carrier for TAB is made from a substrate prepared by forming a copper layer on a polyimide film by additive plating. A photoresist layer is formed on the copper layer, and another photoresist layer on the polyimide film. Both of the photoresist layers are simultaneously exposed to light through a mask applied to each of them to define a desired pattern. The exposed portions of the photoresist layer on the copper layer are subjected to development and postbaking, whereby selected portions of the copper layer are exposed. The exposed portions of the copper layer are additive plated with copper, whereby leads are formed. The exposed portions of the photoresist layer on the polyimide film are subjected to development and postbaking, whereby selected portions of the polyimide film are exposed. The remaining portions of the photoresist layer are removed from the copper layer and the underlying copper layer is etched. The exposed portions of the polyimide film are etched, and the remaining portions of the photoresist layer are removed from the polyimide film.

**EP 0 415 659 A2**

## PROCESS FOR MAKING A TWO-LAYER FILM CARRIER

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention:

This invention relates to a process for making a two-layer film carrier for TAB.

#### 2. Description of the Prior Art:

Although wire bonding has hitherto been used for interconnecting the component parts of an IC package, TAB (tape automated bonding) is now considered as a better method for interconnections for making a multi-pinned type of IC. There are known three types of film carriers for TAB.

(1) Firstly, there is known a one-layer film carrier formed from a single metal, such as copper or aluminum. This type of film carrier can be made at a low cost, but as a base material having a given thickness is worked on directly, it is difficult to make a finely and accurately worked product. This type of film carrier does not lend itself to use for interconnecting the parts of a multi-pinned IC, but has only a limited scope of application.

(2) Secondly, there is known a three-layer film carrier composed of a copper foil, a polyimide film and an adhesive by which the copper foil is bonded to the polyimide film. The use of the adhesive, however, spoils the benefits of use of the polyimide having a high heat resistance and insulating property. Moreover, the manufacture of this type of film carrier has an economical disadvantage, as it requires an expensive punching tool for making sprocket and device holes.

(3) Thirdly, there is known a two-layer film carrier comprising a substrate which consists of a polyimide film and a metal layer formed on a surface of the polyimide film. This type of film carrier has been proposed as an improvement over the two types of film carriers as hereinabove described, and is claimed to enable the interconnections for a multi-pinned IC.

Although no detail is known as to any specific process that is used for making a two-layer film carrier, it is assumed that a process including the following steps is generally employed:

- (a) Forming a photoresist coating having an appropriate thickness on a copper surface;
- (b) Exposing the photoresist to light through a mask defining a desired pattern;
- (c) Developing the exposed photoresist to expose the copper surface in the desired pattern;

- (d) Forming a copper layer by additive plating;
- (e) Dissolving and removing the remaining photoresist and removing the copper layer by flush etching;
- (f) Applying a photoresist onto both sides;
- (g) Exposing the photoresist on a polyimide film to light in a desired pattern;
- (h) Developing the exposed photoresist to expose the selected portions of the polyimide film;
- (i) Etching the exposed portions of the polyimide film;
- (j) Removing the remaining photoresist from the polyimide film; and
- (k) Forming a plated layer of gold or tin, if required.

This process is, however, considered to have a number of problems as will hereinafter be pointed out:

- (1) As the polyimide film remains exposed to the plating solution, etc. for a long time throughout the steps (d) and (e), its property of being wetted with the photoresist becomes so low that the photoresist applied to it during the step (f) is likely to peel off at any time thereafter;
- (2) As the step (g) of exposure to light comes after the formation of leads, the unevenness of the surface on which the leads have been formed causes the scattering of light, resulting in a lower degree of resolution; and
- (3) As the photoresists on the copper and the polyimide film are separately exposed to light, the accurate positioning of the patterns on both sides is difficult to achieve, and the operation is inefficient and is difficult to perform continuously.

These problems allow the process to produce only a two-layer film carrier which is expensive, and yet which cannot satisfactorily be used for interconnecting the parts of an IC package having a multi-pinned layer.

### SUMMARY OF THE INVENTION

Under these circumstances, it is an object of this invention to provide a process which can make an inexpensive two-layer film carrier which can satisfactorily be used for interconnecting the parts of an IC package having a multi-pinned layer.

This object is attained by a process for making a two-layer film carrier from a substrate prepared by forming a copper layer on a polyimide film by additive plating, the process comprising the steps of:

- (1) forming a photoresist layer having an appropriate thickness on one surface of the substrate, and likewise a photoresist layer having an appropriate thickness on the other surface thereof;
- (2) exposing the photoresist layers on both of the surfaces simultaneously to light through a mask applied to each surface to define a desired pattern;
- (3) subjecting the exposed photoresist layer on the copper layer to development to expose selected portions of the copper layer;
- (4) additive plating the exposed portions of the copper layer with copper to form leads;
- (5) subjecting the exposed photoresist layer on the polyimide film to development and postbaking to expose selected portions of the polyimide film;
- (6) removing the remaining photoresist layer from the copper layer and etching the underlying copper layer;
- (7) etching the exposed portions of the polyimide film.; and
- (8) removing the remaining photoresist layer from the polyimide film.

The photoresist layer formed on one surface of the substrate, or the copper layer is preferably one having a thickness of at least 30 microns and formed by covering it with an alkali-developable negative photoresist solution having a viscosity of at least 40 cp and a solid content of at least 20%, and the photoresist layer on the other surface of the substrate, or the polyimide film preferably has a thickness of 2 to 10 microns (step (1)). The postbaking of the developed photoresist on the copper layer (step (3)) is, if necessary, carried out by heating at a temperature of lower than 130°C for a period of 10 to 90 minutes.

It is sometimes desirable or necessary that the copper layer formed on the polyimide film by additive plating be further electroplated with copper. It may also be desirable or necessary that the surfaces of the leads be finished by plating.

According to the process of this invention, the photoresist layers on both surfaces of the substrate are simultaneously exposed to light, and after a desired pattern has been formed, the copper layer on one surface of the substrate is first etched. Therefore, it enables the manufacture of a very accurately processed two-layer film carrier which can be used effectively during the manufacture of a multi-pinned IC package. Moreover, the process can be carried out continuously and thereby make a two-layer film carrier at a low cost.

Other features and advantages of this invention will become apparent from the following description and the accompanying drawings.

## BRIEF DESCRIPTION OF THE DRAWINGS

FIGURES 1-A to 1-H are a series of elevational and sectional views showing the various steps of a process embodying this invention; and FIGURE 2 is a top plan view of a two-layer film carrier made by way of example in accordance with the process of this invention.

## DETAILED DESCRIPTION OF THE INVENTION

The process of this invention is used for making a two-layer film carrier from a substrate comprising a polyimide film and a copper layer formed thereon either by additive plating, or by additive plating and electroplating following it. The process is also applicable to a substrate comprising a polyimide film and a layer of another metal, such as nickel, chromium or silver, which has been formed thereon in a similar way.

It is necessary to control appropriately the characteristic impedances of an IC including the interconnections in order to increase the speed of signal transmission to and from it. In this connection, and also in view of the technical limitation to the interconnections, it is important to achieve an appropriate conductor thickness. This thickness is in the range of 30 to 50 microns. Therefore, it is advisable to form a photoresist layer having a thickness of at least 30 microns on the copper side of the substrate. Any material can be used if it can form a layer having such a thickness. It is, however, likely that a photoresist in the form of a solution may be preferred, as it is easy to handle or use. In such an event, it is desirable to use an alkali-developable negative photoresist solution having a viscosity of at least 40 cp and a solid content of at least 20%.

Any photoresist material can be used for covering the polyimide film to form device and sprocket holes if it has the property of resisting the substance used for etching the polyimide film. A material of the rubber series can, for example, be used. The photoresist layer formed on the polyimide film preferably has a thickness of 2 to 10 microns. Pinholes or other defects are likely to appear in any layer having a smaller thickness, while a layer having a larger thickness may allow only patterning with low accuracy.

A bar coater can, for example, be used for applying a photoresist solution to the copper layer, or polyimide film, though any other appropriate device or method can also be employed. The photoresist which has been applied to each side of the substrate is prebaked. The conditions for such prebaking are known in the art and do not, there-

fore, form a part of this invention.

The photoresist layers on both sides of the substrate are appropriately masked and are simultaneously exposed to light. The simultaneous exposure enables the achievement of a high level of positioning accuracy in patterning on either side and an improved operating efficiency, and contributes to permitting the process to be carried out continuously.

The exposed photoresist on the copper layer is subjected to development before that on the polyimide film is. After development, the photoresist is postbaked. Its postbaking is not essential, but is preferable to promote its drying and improve its adhesion to the substrate. The postbaking of the photoresist on the copper layer is preferably carried out by heating at a temperature of 100° C to 130° C for a period of 10 to 90 minutes, so that it may not exert any adverse effect on the photoresist on the polyimide film. The use of a higher temperature and a longer time would result in the failure of the photoresist on the polyimide film to be satisfactorily developed, while the use of a lower temperature and a shorter time would fail to produce any satisfactory result of postbaking in the photoresist on the copper layer.

After development and postbaking, leads are formed by the additive plating of the exposed portions of the copper layer with copper. The additive plating may be either semi-additive or full-additive.

The photoresist on the polyimide film which has been exposed to light is, then, subjected to development and postbaking. The remaining photoresist is removed from the copper layer and the underlying copper layer is etched. Then, the exposed portions of the polyimide film are etched and the remaining photoresist is removed from the polyimide film. The etching of the copper layer is done before that of the polyimide film, as it does not exert any adverse effect on the polyimide film. Finally, the surfaces of the leads may or may not be finished by plating.

The conditions for the postbaking of the photoresist on the polyimide film, the removal of the photoresist from the copper layer, the etching of the copper layer or the polyimide film, etc. are not particularly specified herein, as they are all known in the art.

The invention will now be described more specifically with reference to a few examples thereof.

#### EXAMPLE 1

A two-layer film carrier was made by a process embodying this invention as shown in FIGURES 1-A to 1-H. The process was applied to a substrate

comprising a polyimide film having a thickness of 50 microns (a product of Dupont-Toray Co., Ltd. sold under the name "Kapton") and a copper layer formed on one surface of the polyimide film 20 by sputtering and having a thickness of one micron (FIGURE 1-A).

An alkali-developable negative photoresist solution having a viscosity of at least 40 cp and a solid content of at least 20% was applied by a bar coater onto the surface of the copper layer 2 and was prebaked at 70° C for 30 minutes to form a prebaked photoresist layer 3 having a thickness of about 45 microns (FIGURE 1-B). Similarly, a negative photoresist solution was applied by a bar coater onto the surface of the polyimide film 1 and was prebaked at 70° C for 30 minutes to form a prebaked photoresist layer 3 having a thickness of about five microns (FIGURE 1-B).

The photoresist layers 3 on both sides of the substrate were simultaneously exposed to light through a mask applied to each side in accordance with a selected pattern. The exposed portions of the photoresist 3 on the copper layer 2 were subjected to development, and postbaked at 110° C for 30 minutes, whereby selected portions of the copper layer 2 were exposed (FIGURE 1-C). The exposed portions of the copper layer 2 were electroplated with copper at a current density of 2 A/dm<sup>2</sup> for a period of 50 minutes, whereby leads having a thickness of about 35 microns were formed as shown at 4 (FIGURE 1-D).

Those portions of the photoresist 3 on the polyimide film 1 which had been exposed to light were subjected to development, and postbaked at 130° C for 30 minutes, and the substrate was dipped in a 4% NaOH solution having a temperature of 60° C and left to stand for one minute, whereby the remaining photoresist 3 was removed from the copper layer 2 (FIGURE 1-E). Then, it was dipped in a cuprous chloride solution having a temperature of 50° C and left to stand for 30 seconds, whereby the underlying copper layer 2 was dissolved away (FIGURE 1-F).

The substrate was, then, dipped in an etching solution prepared by mixing hydrazine hydrate and ethylenediamine in a ratio by volume of 8:2, and having a temperature of 50° C, and was left to stand for five minutes, whereby various holes 5 to 7 (sprocket, device and outer lead holes) were formed through the polyimide film 1 (FIGURE 1-G). Then, the remaining photoresist 3 was removed from the polyimide film 1 by using a solvent containing 20% by volume of each of chlorobenzene and tetrachloroethylene.

Finally, the lead surfaces were given a finish by three minutes of gold plating at a current density of 1 A/dm<sup>2</sup>, whereby there was obtained a two-layer film carrier having a lead width of 70 microns

and a space width of 60 microns (FIGURE 1-H).

## EXAMPLE 2

A two-layer film carrier was made by repeating the process of EXAMPLE 1, except that the substrate consisted of a polyimide film having a thickness of 50 microns ("Kapton" of Dupont-Toray Co., Ltd.) and a copper layer consisting of a layer formed on the polyimide film by electroless plating and having a thickness of 0.25 micron and a layer formed on the electrolessly plated layer by electroplating and having a thickness of one micron, and that the copper layer was coated with the negative photoresist solution sold by Tokyo Ohka Kogyo Kabushiki Kaisha under the designation "PMER HC 600", and the polyimide film with the negative photoresist sold by Fuji Yakuhin Kogyo Kabushiki Kaisha under the designation "FSR".

It was similar to the product of EXAMPLE 1, and its top plan was as shown in FIGURE 2, in which the two-layer film carrier is shown at 9, its sprocket holes at 5, its device hole at 6, and its inner leads at 8.

## Claims

1. A process for making a two-layer film carrier from a substrate prepared by forming a copper layer on a polyimide film by additive plating, said process comprising the steps of:

- (1) forming a photoresist layer having an appropriate thickness on said copper layer, and a photoresist layer having an appropriate thickness on said polyimide film;
- (2) exposing both of said photoresist layers simultaneously to light through a mask applied to each of them to define a desired pattern;
- (3) subjecting said exposed photoresist layer on said copper layer to development to expose selected portions of said copper layer;
- (4) additive plating said exposed portions of said copper layer with copper to form leads;
- (5) subjecting said exposed photoresist layer on said polyimide film to development and post-baking to expose selected portions of said polyimide film;
- (6) removing the remaining photoresist layer from said copper layer and etching the underlying copper layer;
- (7) etching said exposed portions of said polyimide film; and
- (8) removing the remaining photoresist layer from said polyimide film.

2. A process as set forth in claim 1, wherein said

copper layer formed by additive plating is further electroplated with copper.

3. A process as set forth in claim 1 or 2, wherein said photoresist layer on said copper layer is formed by covering said copper layer with an alkali-developable negative photoresist solution having a viscosity of at least 40 cp and a solid content of at least 20% and prebaking it, while said photoresist layer on said polyimide film is formed by covering said film with a photoresist of the rubber series and prebaking it.

4. A process as set forth in claim 3, wherein said thickness of said photoresist layer on said copper layer is at least 30 microns, while said thickness of said photoresist layer on said polyimide film is from two to 10 microns.

5. A process as set forth in claim 1 or 2, wherein said copper layer which was developed is further subjected to postbaking at a temperature of lower than 130° C for a period of 10 to 90 minutes so as to expose selected portions of said copper layer.

6. A process as set forth in claim 1 or 2, further including the steps of:

- (9) giving said leads a surface finish by plating.

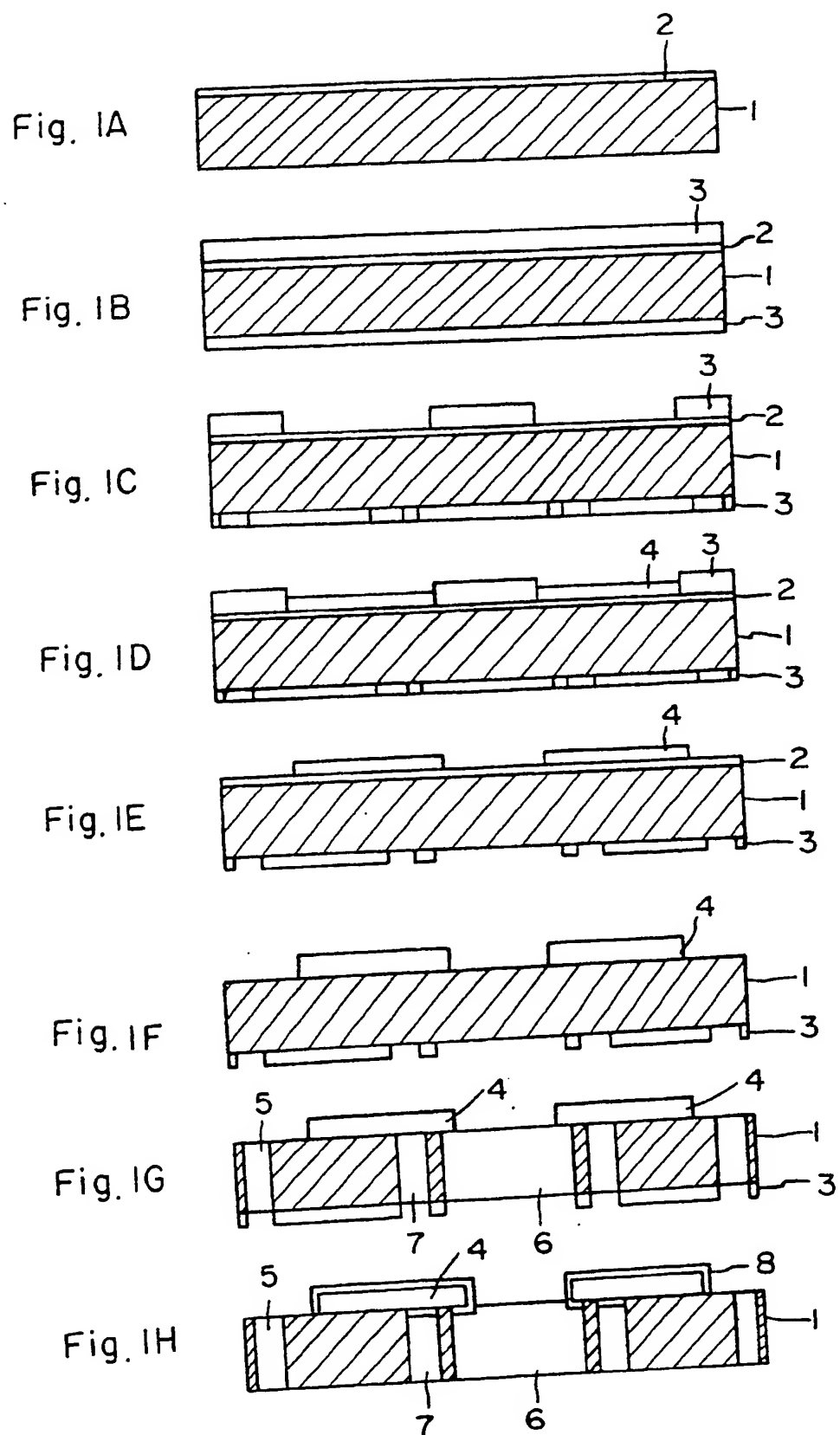
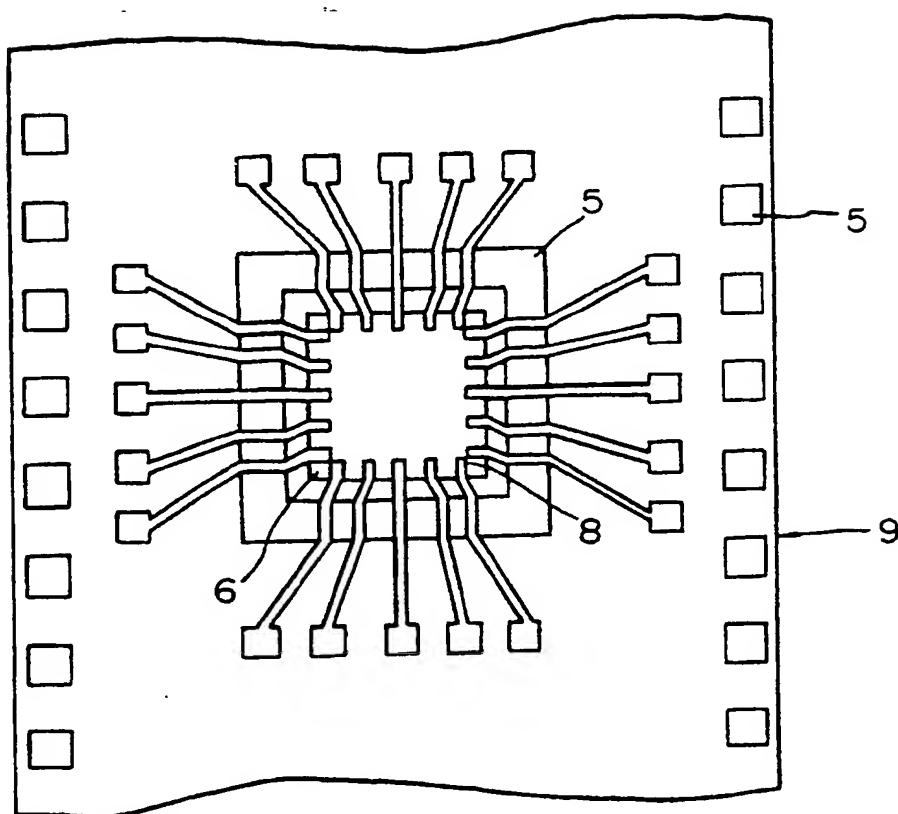
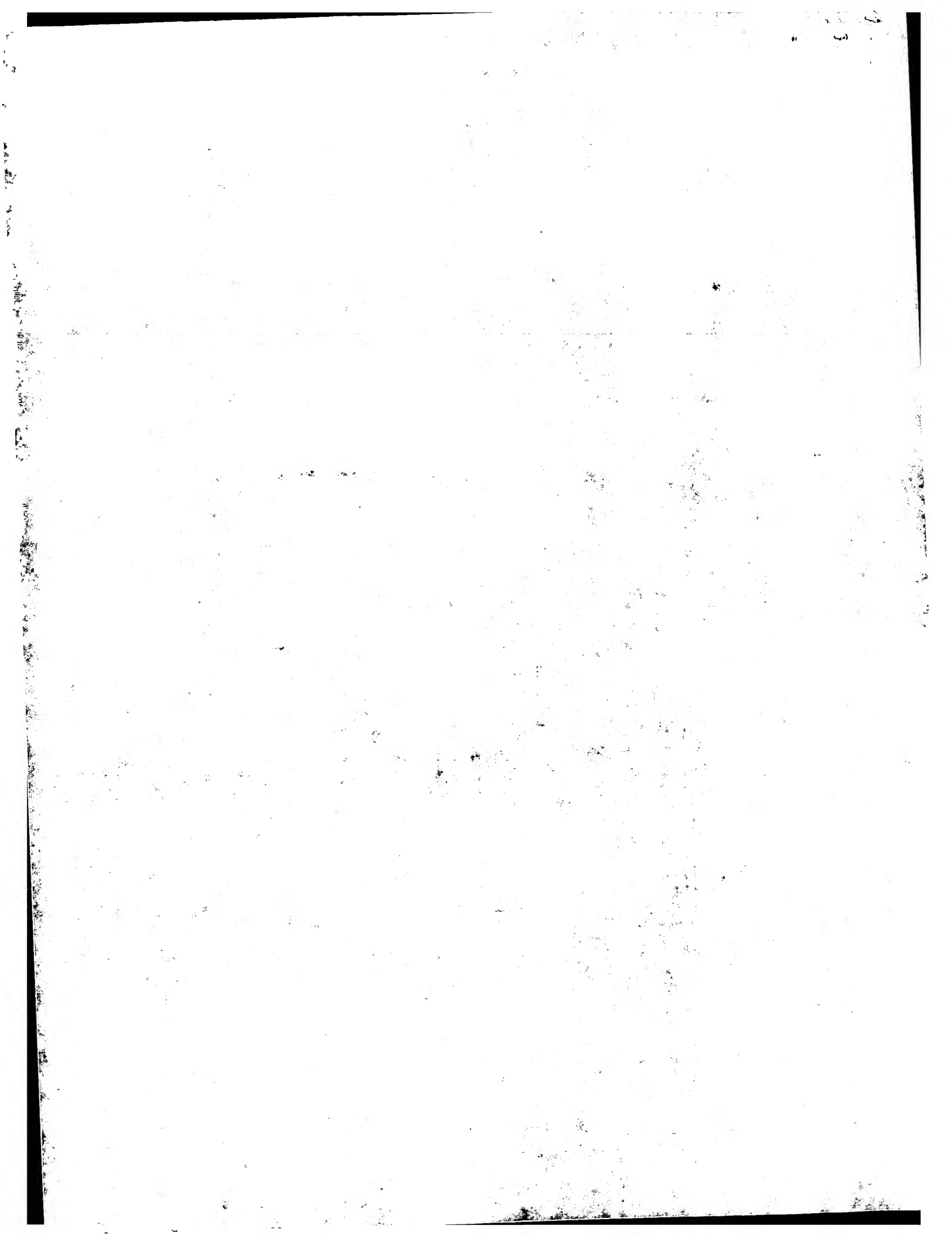


Fig. 2









Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) Publication number:

**0 415 659 A3**

(12)

## EUROPEAN PATENT APPLICATION

(21) Application number: 90309293.0

(51) Int. Cl.<sup>5</sup>: H01L 21/48, H01L 23/498

(22) Date of filing: 24.08.90

(30) Priority: 28.08.89 JP 218723/89

(43) Date of publication of application:  
06.03.91 Bulletin 91/10

(84) Designated Contracting States:  
DE FR GB

(89) Date of deferred publication of the search report:  
18.09.91 Bulletin 91/38

(71) Applicant: SUMITOMO METAL MINING  
COMPANY LIMITED  
11-3, Shinbashi 5-chome Minato-ku  
Tokyo 105(JP)

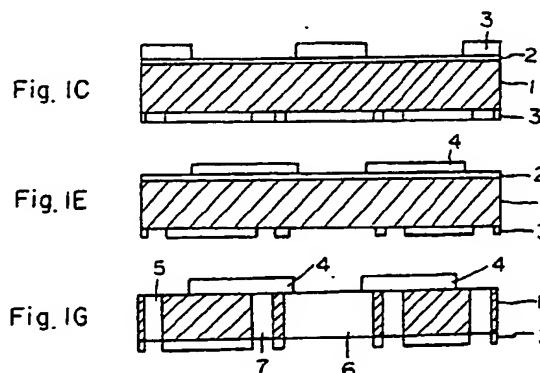
(72) Inventor: Chonan, Takeshi  
3-9-1 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)  
Inventor: Kudo, Yuko  
2-1-8 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)  
Inventor: Hirota, Yoshihiro  
3-18-35 Nakakokubun  
Ichikawa City, Chiba Pref.(JP)

(74) Representative: Hartley, David et al  
Withers & Rogers 4 Dyer's Buildings Holborn  
London, EC1N 2JT(GB)

(54) Process for making a two-layer film carrier.

(57) A two-layer film carrier for TAB is made from a substrate (1) prepared by forming a copper layer (2) on a polyimide film by additive plating. A photoresist layer (3) is formed on the copper layer, and another photoresist layer (3) on the polyimide film. Both of the photoresist layers are simultaneously exposed to light through a mask applied to each of them to define a desired pattern. The exposed portions of the photoresist layer on the copper layer are subjected to development and postbaking, whereby selected portions of the copper layer are exposed. The exposed portions of the copper layer are additive plated with copper, whereby leads (4) are formed. The exposed portions of the photoresist layer on the polyimide film are subjected to development and postbaking, whereby selected portions of the polyimide film are exposed (5, 6, 7). The remaining portions of the photoresist layer are removed from the copper layer and the underlying copper layer is etched. The exposed portions of the polyimide film are etched, and the remaining portions of the photoresist layer are removed from the polyimide

film.



EP 0 415 659 A3



European  
Patent Office

## EUROPEAN SEARCH REPORT

Application Number

EP 90 30 9293

### DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	GB-A-2 178 231 (QUICK TURNAROUND LOGIC LIMITED) * page 2, line 94 - page 3, line 4; figure 4 *	1,6	H 01 L 21/48 H 01 L 23/498
Y	GB-A-2 169 553 (ROGERS CORPORATION) * page 1, lines 73 - 94; figures 1, 2 *	1,6	
A	US-A-3 838 984 (J.R.CRANE) * column 4, line 13 - column 5, line 60; figures 5, 6 *	1,6	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H 01 L
The present search report has been drawn up for all claims			
Place of search		Date of completion of search	Examiner
Berlin		05 July 91	LE MINH I
<b>CATEGORY OF CITED DOCUMENTS</b> X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document			